



#7

PATENT  
1248-0509P

## IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant: Kumi MIYACHI et al. Conf.: 3629  
Appl. No.: 09/615,956 Group: 2133  
Filed: July 13, 2000 Examiner: DECADY  
For: SEMICONDUCTOR DEVICE HAVING INTEGRALLY  
SEALED INTEGRATED CIRCUIT CHIPS ARRANGED  
FOR IMPROVED TESTING

RECEIVED

DEC 03 2002

LETTER TO THE OFFICIAL DRAFTSPERSON

Technology Center 2100

Assistant Commissioner for Patents  
Washington, DC 20231

November 27, 2002

Sir:

Attached hereto is/are seven (7) sheet(s) of formal drawings which comply with the provisions of 37 C.F.R. § 1.84. The drawings should be made a part of the record of the above-identified application.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. §§1.16 or 1.17; particularly, extension of time fees.

Respectfully submitted,

BIRCH, STEWART, KOLASCH &amp; BIRCH, LLP

By   
Charles Gorenstein, #29,271

RWD  
CG/RWD/mag  
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Attachments